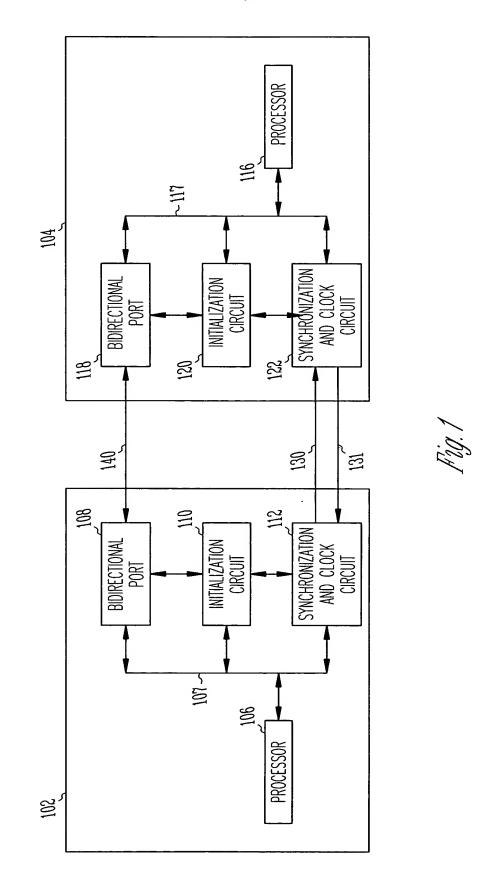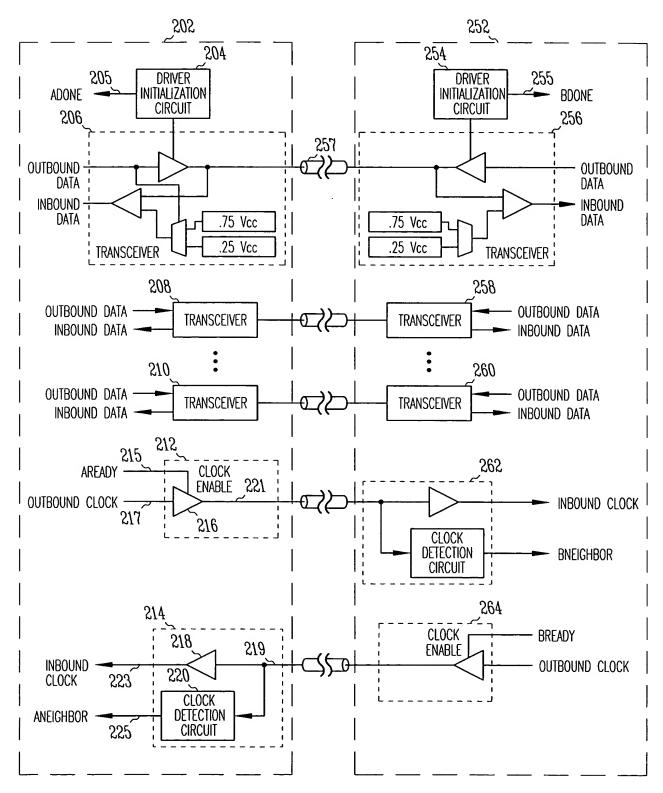TITLE: BIDIRECTIONAL PORT WITH CLOCK CHANNEL USED FOR SYNCHRONIZATION
INVENTORS NAME: Matthew B. Haycock et al.
DOCKET NO.: 884.509US2

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100

104

117

118 BIDIRECTIONAL PORT

120 INITIALIZATION CIRCUIT

122 SYNCHRONIZATION AND CLOCK CIRCUIT

116 PROCESSOR

140

130

131

102

108 BIDIRECTIONAL PORT

110 INITIALIZATION CIRCUIT

112 SYNCHRONIZATION AND CLOCK CIRCUIT

107

106 PROCESSOR

Fig. 1

TITLE: BIDIRECTIONAL PORT WITH CLOCK CHANNEL USED FOR SYNCHRONIZATION
INVENTORS NAME: Matthew B. Haycock et al.
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Fig.2

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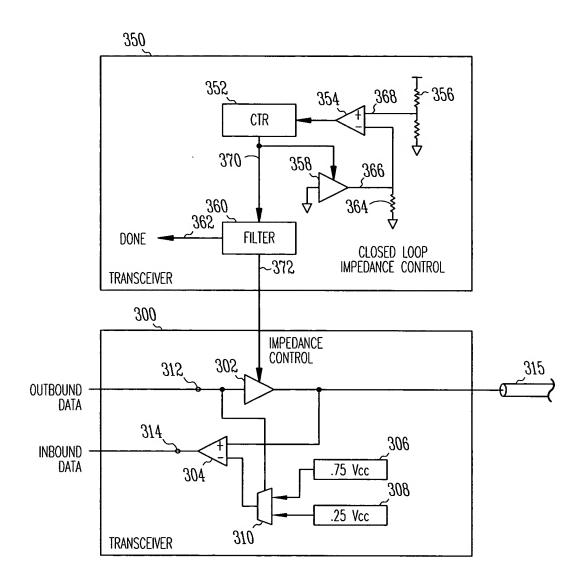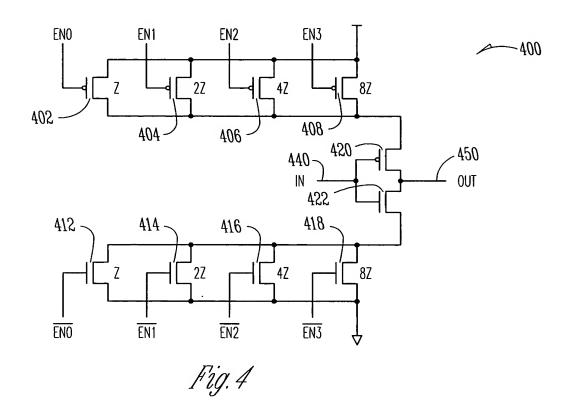INVENTORS NAME: Matthew B. Haycock et al.
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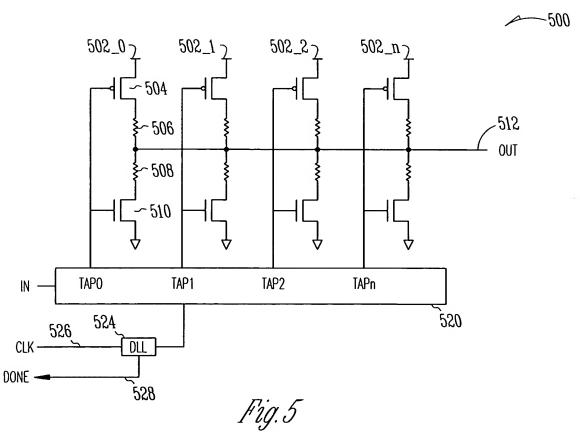*Fig.3*

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Fig. 4

Fig. 5

TITLE: BIDIRECTIONAL PORT WITH CLOCK CHANNEL USED FOR SYNCHRONIZATION
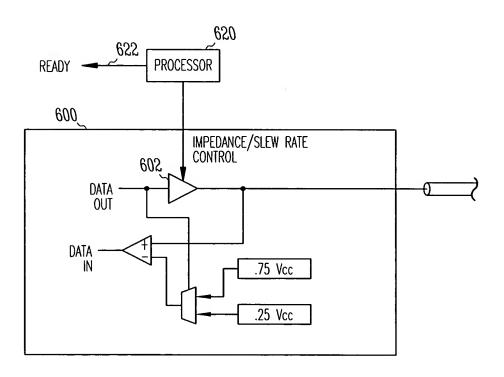INVENTORS NAME: Matthew B. Haycock et al.
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620

622   PROCESSOR

READY

600

IMPEDANCE/SLEW RATE
CONTROL

602

DATA
OUT

DATA
IN

+
−

.75 Vcc

.25 Vcc

Fig. 6

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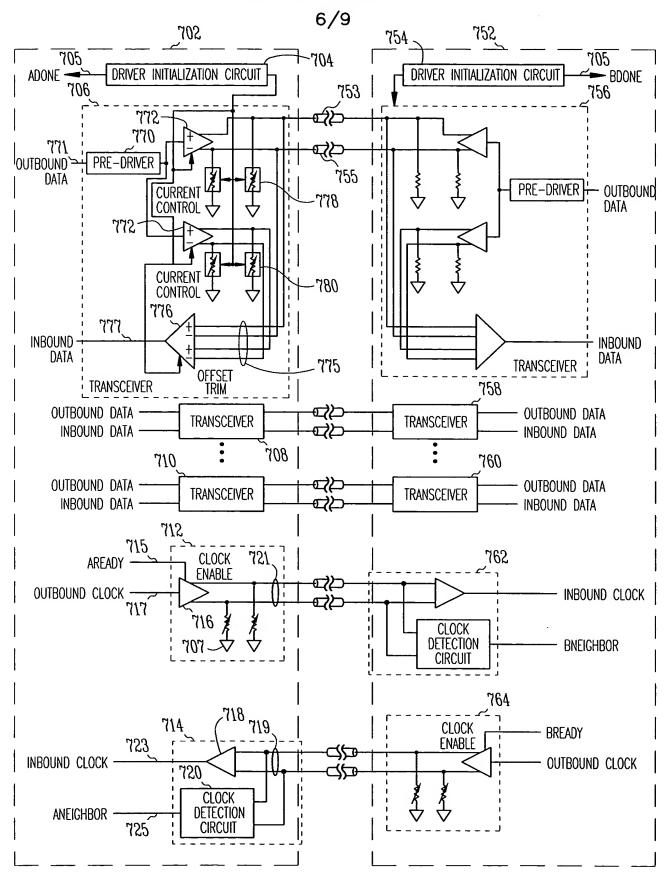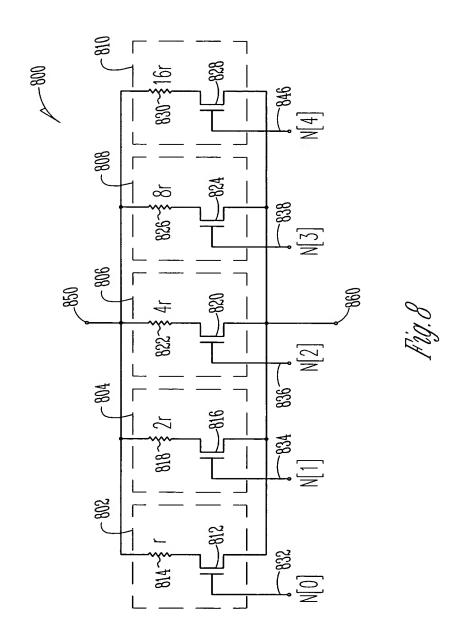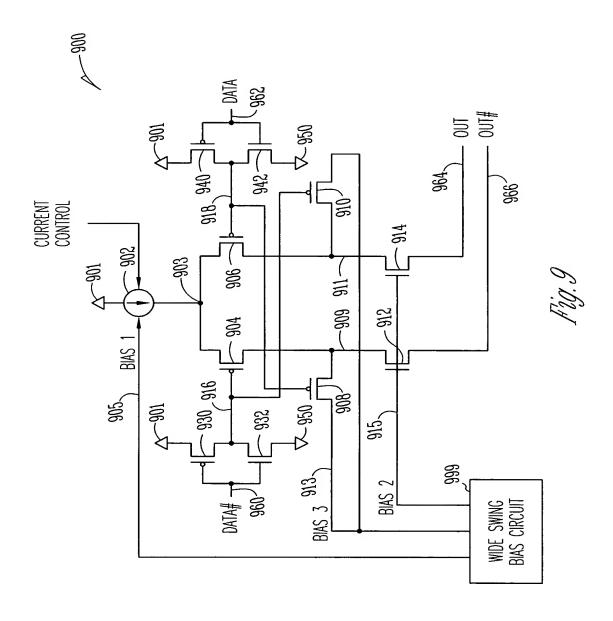*Fig. 7*

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Fig.8

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Fig. 9

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Fig. 10